



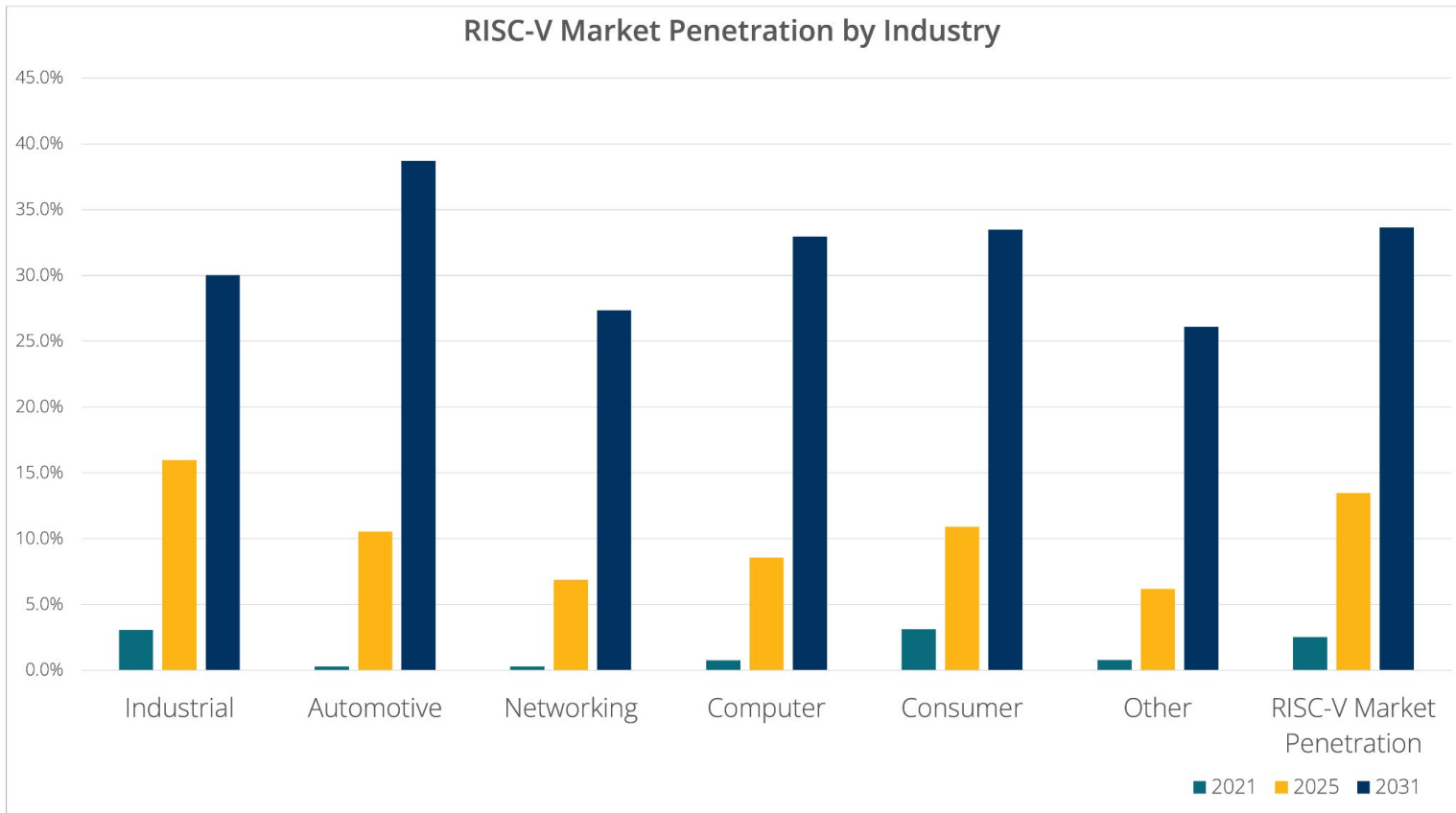
# RISC-V in Space

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RISC-V turned 15 last year!



# From 2.5% to 33.7% in 10 years!





# RVA23 Momentum

	5000 series
	AndesCore™ AX66
	Cuzco
	XuanTie C930
	S500 series
	UX1030H
	P800 series
	X100 and K3
	Ascalon
	Veyron V2, V3

## Processors



Plans to port CUDA to RISC-V RVA23



RVA23 baseline for the Android RISC-V ABI



Support RVA23 from Ubuntu 25.10



Developer preview of RHEL 10



RISC-V primary supported target in Yocto 5.3

## Software

# RISC-V is now an ISO/IEC JTC1 PAS Submitter



**JTC1**



**INFORMATION TECHNOLOGY STANDARDS**

Ballot passed in September 2025

# RISC-V is now an ISO/IEC JTC1 PAS Submitter!



Ballot passed in September 2025

- RISC-V International (as a group of members) operates in line with the ISO/IEC JTC1 directives
- Policies and guidelines, our spec development process, our tech committees and voting procedures are fully aligned to JTC1
- RISC-V International is an entity eligible to submit specifications to become ISO/IEC JTC1 published standards
- We will now work to make the RISC-V ISA manuals become ISO/IEC JTC1 standards

# Priority Industry Verticals & Horizontals



Automotive



Data Center



Embedded / IoT



Consumer



HPC



Industrial



Space / Aerospace

## Verticals



AI



Security

## Horizontals

Ten months ago...

**FRONTGRADE**



# RISC-V IN SPACE WORKSHOP

2-3 April 2025

Gothenburg, Sweden

**Hundreds of speakers and  
hundreds of RISC-V projects!!!!**



## Space

- RISC-V now leading ISA in segment
  - NASA HPSC SoC Platform RISC-V based
  - ESA/Frontgrade Gaisler RISC-V Space MCU
- RISC-V meets tech sovereignty concerns
- Aligns with new Private/Public sector cooperation paradigm
- Draws upon and opens opportunities for growth in adjacent segments

# Join the newly established RISC-V Space SIG

## Goals

- Gap Analysis & Remediation
- Profile Definition for payload processing and platform control
- Security Architecture
- Ecosystem Acceleration

## Deliverables

- Space Domain Gap Analysis Report
- Space-Grade Profile Recommendations
- Space Security Threat Model
- Ecosystem Landscape Guide

Gianluca Furano, ESA, Chair

Fabrizio Magugliani, E4 Computing, Vice Chair



# RISC-V Future Goals 1H26

## Technical Priorities

- Matrix Extensions
- Security
- Improved standards compliance





# Thank you!

<https://riscv.org/risc-v-space-and-aerospace/>